

Notice of Allowability

Application No.

10/789,673

Examiner

Tuan T. Nguyen

Applicant(s)

CHOI, JOO S.

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the Amendment filed on 7/17/06.
2. ☒ The allowed claim(s) is/are 1, 6, 8, 13, 20, 27-30, 35-49.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

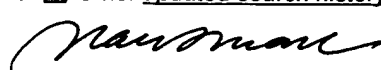
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other updated search history.

 8/21/06

TUAN T. NGUYEN
PRIMARY EXAMINER

DETAILED ACTION

This is the response to Applicant's Amendment filed on 7/17/06.

Claims 2-5, 7, 9-12, 14-19, 21-26 and 31-34 have been canceled. Claims 35-49 have been added.

Examiner has approved the amended specification and drawings submitted on 7/17/06.

EXAMINER'S AMENDMENT

The application has been amended as follows:

Claim 35, line 5, change "a transfer a" to ' transfer a"

Allowable Subject Matter

Claims 1, 6, 8, 13, 20, 27 allowed. Claims 1, 8, 20, 35, 37 and 43 are independent claims.

The following is an examiner's statement of reasons for allowance:

The prior art of record fails to disclose a memory system, in combination with other cited limitations, comprising time delay information operable to at least approximately center data communicated to and from the memory device within a data eye as recited in claim 1.

The prior art of record further fails to disclose a memory system, in combination with other cited limitations, comprising a data eye learning block operable to generate a time delay interval signal, and the data eye learning block communicates the time delay interval signal to the memory device on the first signal path as recited in claim 8.

The prior art of record also fails to disclose a memory system, in combination with other cited limitations, comprising a data eye learning block that receives output data from the memory

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device and generates a time delay value that at least approximately aligns the output data within a data eye, the time delay value being communicated to the memory device on the signal path as recited in claim 20.

The prior art of record further fails to disclose a memory system, in combination with other cited limitations, comprising a signal path being operable to transfer a selected memory system characteristic between the controller and the memory device, wherein the selected memory system characteristic further comprises parity information generated within the memory device as recited in claim 35.

The prior art of record further fails to disclose a memory system, in combination with other cited limitations, comprising a plurality of data lines coupled to the plurality of memory cells and a first comparison gate coupled to the data lines, the first comparison gate being operable to successively compare a logic state on each of the plurality of data lines and to generate a first parity value based on the successive comparison, the first parity value being communicated to the controller on the first signal path as recited in claim 37.

The prior art of record further fails to disclose a memory system, in combination with other cited limitations, comprising a first comparison gate within the memory device that is coupled to data lines and generates a first parity value therefrom, the first parity value being communicated to the controller on the signal path as recited in claim 40.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Nguyen whose telephone number is 571-272-1880. The examiner can normally be reached on Monday - Friday, 8AM - 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on 571-272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



Tuan T Nguyen
Primary Examiner
Art Unit 2824

August 21, 2006

TUAN T. NGUYEN
PRIMARY EXAMINER